--30. A liquid crystal device, comprising:

a liquid crystal substrate including a display region in which pixels are formed in a matrix by a plurality of light shielding data lines and a plurality of scanning lines, a peripheral driving circuit connected to at least one of the data lines and the scanning lines on an outer peripheral side of the display region, and a plurality of thin film transistors connected to the data lines and the scanning lines, and a liquid crystal held between the liquid crystal device substrate and an opposite substrate:

a conductive first light shielding film provided above the liquid crystal substrate, the conductive first light shielding film having a channel light shielding portion for shielding a channel region of the thin film transistor and a peripheral portion formed in the peripheral of pixel regions that is electrically connected to the channel light shielding portion, the conductive first light shielding film extending along with either the data lines or the scanning lines; and

a constant potential wiring being connected to the conductive first light shielding film.--

a peripheral partitioning light shielding film formed in the pro-

peripheral region of the conductive first light shielding film.--

--32. The liquid crystal device according to claim 30, wherein the channel light shielding portion of the conductive first light shielding film is overlapped with the seanning line and a capacitor line.--